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**PATENT APPLICATION
Attorney Docket No. OR00-12101**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)

Tirthankar Lahiri et al.)

Group Art Unit: 2121

U.S. Patent No.: 6,952,664 B1)

Issued: 4 October 2005)

Application No.: 09/834,342)

Examiner: Mai, Tan V.

Filed: 13 April 2001)

For: SYSTEM AND METHOD FOR)

PREDICTING CACHE PERFORMANCE)

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate of Correction Branch of the Publishing Division
Assistant Commissioner for Patents
PO Box 1450
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Certificate

OCT 24 2005

of Correction

Dear Sir:

We hereby request that the above-referenced U.S Patent be corrected in the following way:

In claim 9 (at column 11, line 17), please delete the phrase, "data to"
and replace with the phrase --data cache to--.

In claim 9 (at column 11, line 19), please delete the phrase, "miss for"
and replace with the phrase --miss rate for--.

Please refer to enclosed PTO form PTO/SB/44.

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Respectfully submitted,

By



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Date: 17 October 2005

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,952,664 B1

DATED : 4 October 2005

INVENTOR(S): Tirthankar Lahiri

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In claim 9 (at column 11, line 17), please delete the phrase, "data to" and replace with the phrase --data cache to--.

In claim 9 (at column 11, line 19), please delete the phrase, "miss for" and replace with the phrase --miss rate for--.

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PATENT NO: 6,952,664 B2

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